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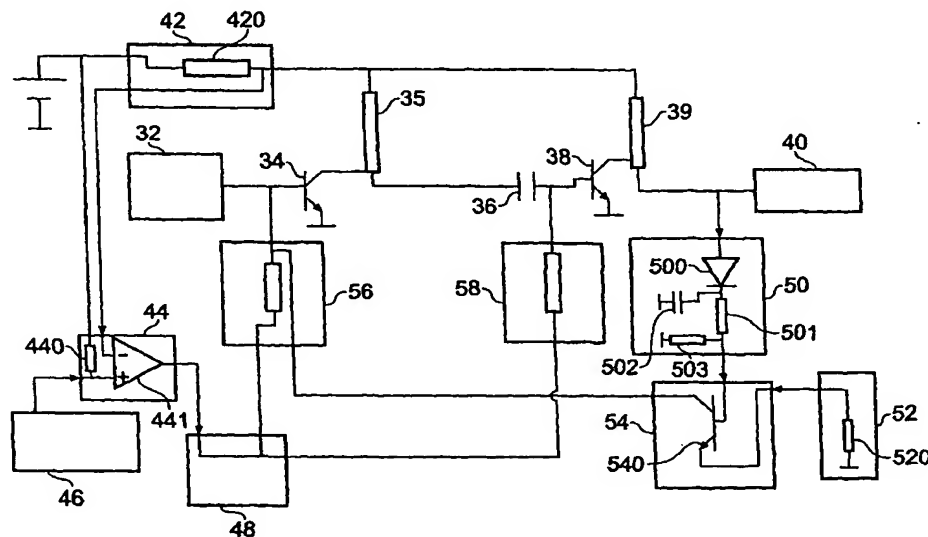
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(54) Title: CIRCUIT FOR POWER AMPLIFICATION



(57) **Abstract:** A radio frequency (RF) power amplifier, including an external control loop and a protection circuit, which functions to maintain a constant output power during variations in impedance on the RF load. In the external control loop a collector current from an output transistor is detected and then regulated with respect to a reference current. The regulated signal is utilised to generate a bias control signal which is input to the base electrode of both a driver transistor and the output transistor. The protection circuit detects a voltage envelope at the collector electrode of the output transistor and utilises this signal to form a bias reduction signal which is input to the base electrode of the driver transistor.

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